NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR

(Institute of National Importance)

Telephone :+91-771-2254200
Fax: +91-771-2254600

G.E. Road, Raipur –492010(C.G)
Department of Electrical Engineering

ADVERTISEMENTNO:NITRR/EE/CPRI-RSOP/JRF/2023/001 **Dated: 10-03-2023**

ADVERTISEMENT FOR WALK-IN-INTERVIEW FOR JUNIOR RESEARCH FELLOW/ PROJECT ASSOCIATE FOR CPRI-RSOP PROJECT

Applications are invited for a **walk-in interview on 29/03/2023, Reporting time 11:00A.M** for the post of a **Junior Research Fellow (1Nos.)** in the Department of Electrical Engineering, National Institute of Technology, Raipur, Chhattisgarh, India. Interested applicants may e-mail their duly filled application form, scanned documents pertaining to educational qualifications, age proof, Essential qualifications as single PDF file to the undersigned with e-mail subject marked as **"Application for Junior Research Fellow/Project Associate for CPRI-RSOP Project" to (ayadav.ele@nitrr.ac.in) by 28/03/2023.**

Name of the position:	Junior Research Fellow (JRF)/Project Associate
Project Title:	**"Design and Development of Artificial Intelligence Based Short Term Load Forecasting Model"**
Funding Agency:	**Central Power Research Institute Bangalore**
Tenure of Scheme & Assignment:	Duration of the project is 2 years, Initially the appointment for JRF is for one year which will be further extended to 2nd year based on the performance
Age	Less than 35 years
Essential qualifications:	i. M.Tech./M.E. or equivalent in Electrical Engineering, Power system, Power system and control, Power Electronics, Electrical Drives, Energy Systems Engineering, Artificial Intelligence & Machine Learning, Computer Science and Data Processing, Electronics and Communication, Information Technology or related/allied fields selected through a process described through any one of the following:- a. Scholars who are selected through National Eligibility Tests - CSIR-UGC NET including lectureship (Assistant Professorship) or GATE. b. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR,. ICMR, IIT, lISc, IISER etc. Note: Gross emolument Rs. 31,000/- + HRA **Or** ii. B.Tech./B.E. in Electrical Engineering, Electrical and Electronics Engineering, Power Engineering, Computer Science, Electronics and Communication, Information Technology or equivalent and related disciplines with first class. (a) Gross emolument Rs. 31,000/- + HRA to Scholars who are selected through (a) National Eligibility Tests - CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions. (b) Gross emolument Rs. 25,000/- + HRA for others who do not fall under (a) above. Note:- Candidates having good knowledge of MATLAB and Artificial Intelligence & Machine Learning in will be given preference.
Date, time and placeof Interview:	**29/03/2023 at reporting time 11:00 AM** in the **Department of Electrical Engineering**, **National Institute of Technology Raipur, India.**
Note	Selected candidate if having M.Tech./M.E. degree will be encouraged to enroll for Ph.D. at NIT Raipur as per rules and regulations of NIT Raipur.

Terms and Conditions:

1. The above position is purely temporary and continuation of the fellowship is subject to satisfactory performance and review after each year.

2. Candidate fellowship can be terminated with one-month prior notice given by the institution. Similarly, candidate can also give prior notice of one month before leaving the fellowship or has to deposit one-month fellowship amount.

3. Candidates are required to appear for the interview with photocopy of the application form, two passport size photos, curriculum vitae, testimonials and all relevant original certificates with a one set of self-attested photocopies of the certificates.

4. No TA/DA is permissible for appearing ininterview.

5. Selected candidates will not be permitted to claim for any regular/part time appointments in this institute or any other institutes during this period.

6. The above position is purely temporary, and continuation of the fellowship is subject to satisfactory performance and review after every 3 months and availability of fund underproject.

7. Canvassing in any form will disqualify the candidature and the appointment shall be co-terminus with the project.

For further details, please contact:

Dr. Anamika Yadav (Principal Investigator)

Associate Professor & HOD

Department of Electrical Engineering
NIT Raipur, GE Road, Raipur, Chhattisgarh- 492010
Email:**ayadav.ele@nitrr.ac.in** **(9425852654)**

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Department of Electrical Engineering

APPLICATION FOR THE POST OF JUNIOR RESEARCH FELLOW/ PROJECT ASSOCIATE FOR CPRI-RSOP PROJECT
ADVERTISEMENT NO: NITRR/EE/CPRI-RSOP/JRF/ PROJECT ASSOCIATE /2023/001

ADVT. NO…………………………........ **DATE**…………………………

TITLE OF PROJECT …………………………………………………………………

……………………………………………………………………………………..

Attach Passport Size Self-attested Photograph

NAME OF CANDIDATE (in Block letter):						
DOB :						
AGE AS ON CLOSING DATE OF THE APPLICATION:						
CATEGORY						
CORRESPONDING ADDRESS: (WITH CONTACT NUMBER & E-MAIL ID)						
FATHER'S NAME :						
QUALIFICATION: *(ATTACH CERTIFICATES ALSO)	Sl. No	Name of the Examination	Board / University	% of Marks	Year	Division
	1	10th				
	2	12th				
	3	B. Tech				
	4	M. Tech				
WORK EXPERIENCE (IN DETAIL): (Attach Certificate(s) also)						
GATE / NET (qualifications:) :Marks obtained/Percentile or AIR						
Any other relevant information :						

UNDERTAKING

I understand that it is a contractual engagement on purely temporary basis for the Project. I declare that the above particulars are correct to the best of my knowledge and belief. I understand that action can be taken against me if these are found to be incorrect.

Place:

Date: **SIGNATURE OF THE CANDIDATE**

*Please enclose relevant self-attested certificates.